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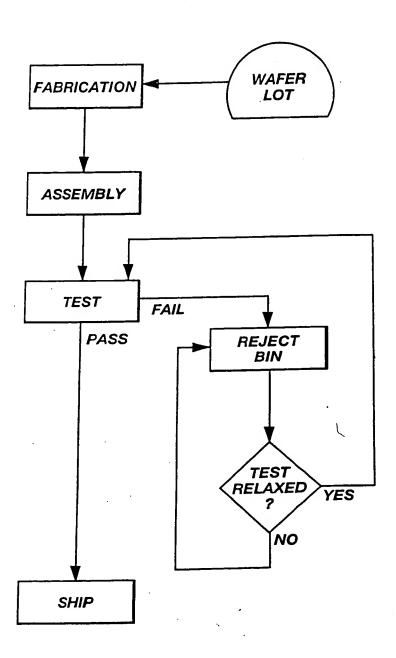


Fig. 1 (PRIOR ART)

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TITLE: METHOD FOR SORTING INTEGRATED CIRCUIT DEVICES

Inventor: Raymond J. Beffa Docket No.: 3037.10US



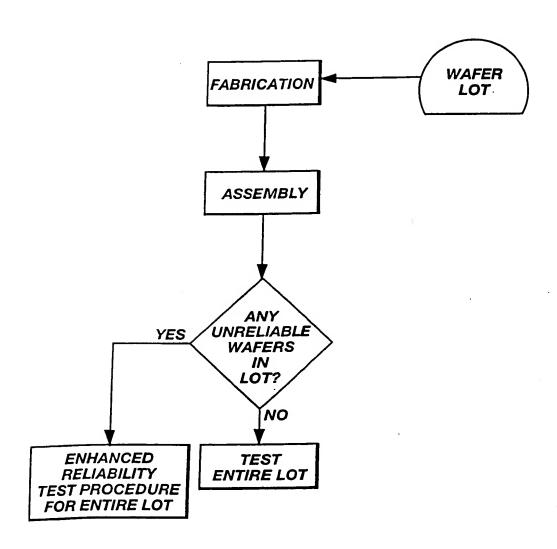


Fig. 2 (PRIOR ART)

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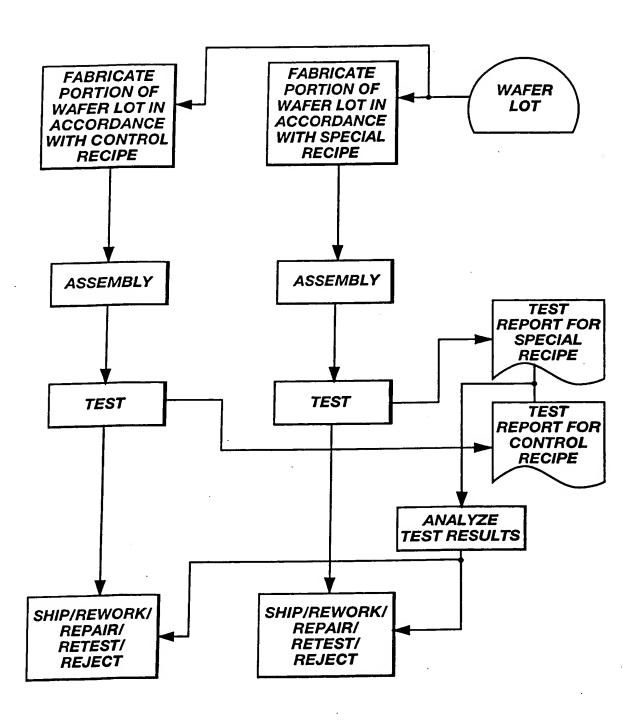


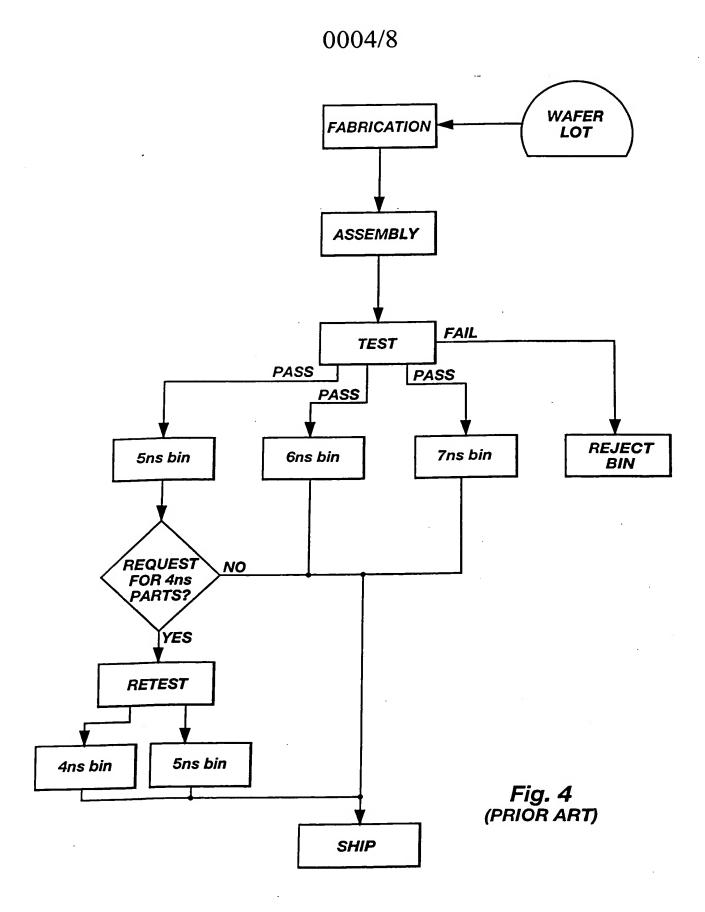
FIG. 3 (PRIOR ART)

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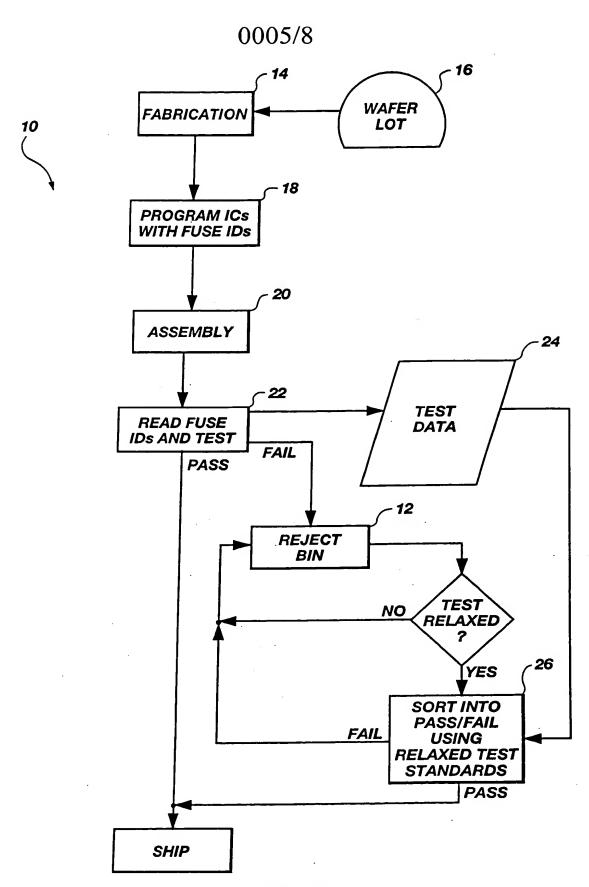


Fig. 5

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John H. H. Wood H. H. Jeen Hall

Inventor: Raymond J. Beffa Docket No.: 3037.10US



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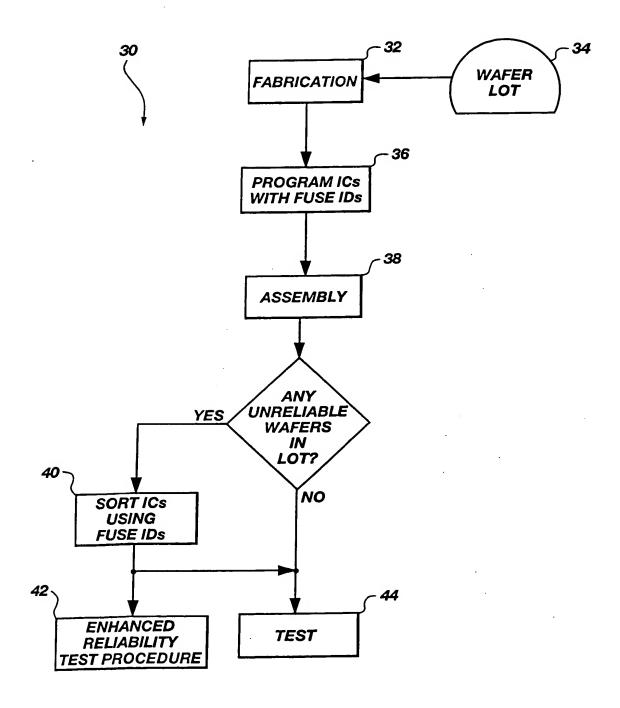
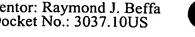
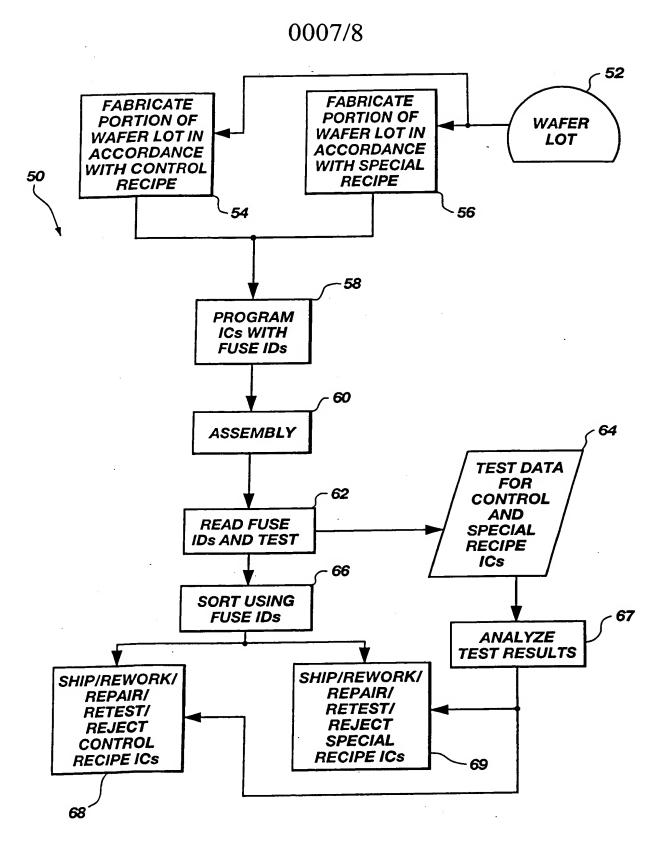


Fig. 6

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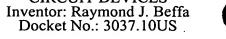
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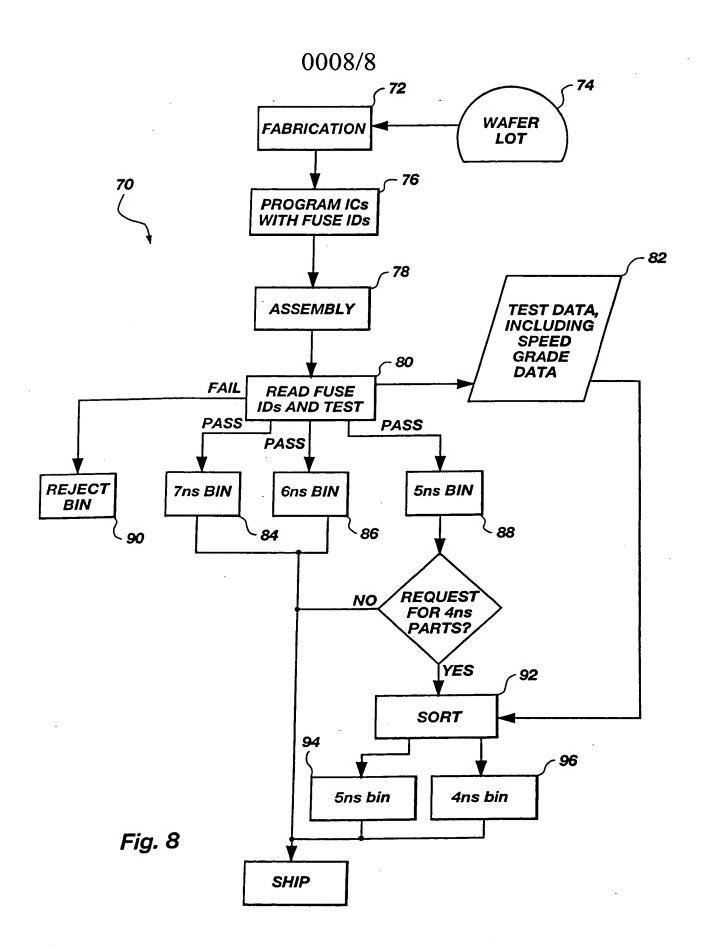
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Fig. 7





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